

11/14/03

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				Applicati n Number	10 713 852
				Filing Date	Filed Herewith 11/14/03
				First Named Inventor	Asli B. Ucok
				Group Art Unit	2815
				Examiner Name	Jasmine clark
Sheet	2	of	2	Attorney Docket Number	UOM 0319 PUS
OTHER PRIOR ART -- NON PATENT LITERATURE DOCUMENTS					
Examiner Initials *	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.			T ²
jbe		AL-SARAWI, SAID F., ET AL., A Review of 3-D Packaging Technology, IEE Transactions On Components, Packaging And Manufacturing Technology, 21, No. 1, pp. 2-14, February 1998			
		SCHUENEMANN, MATTHIAS, ET AL., MEMS Modular Packaging And Interfaces, Proc. Of The 50 th Electronic Components And Technology Conference, pp. 681-688, 2000			
		MILLER, DAVID C., ET AL., Microrelay Packaging Technology Using Flip-Chip Assembly, Proc. Of The 13 th International Conference On Micro-Electro-Mechanical Systems, pp. 265-270, Miyazaki, Japan, January 23-27, 2000			
		MASON, ANDREW, ET AL., A Generic Multi-Element Microsystem For Portable Wireless Applications, Proc. Of The IEEE, pp. 1733-1746, August, 1998			
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		GANN, KEITH D., Neo-Stacking Technology, HDI Magazine, December, 1999			
		BAKIR, MUHANNAD S., ET AL., Sea of Leads (SOL) Ultrahigh Density Wafer-Level Chip Input/Output Interconnections For Gigascale Integration (GSI), IEEE Transactions On Electron Devices, Vol. 50, No. 10, October, 2003			
jbe		MURARI, BRUNO, Bridging The Gap Between The Digital And Real Worlds: The Expanding Rule of Analog Interface Technologies, 2003 IEEE International Solid-State Circuits Conference, Plenary Session 1.3, February 10, 2003			

Examiner Signatur	Jasmine clark	Date Consid red	12/08/04
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